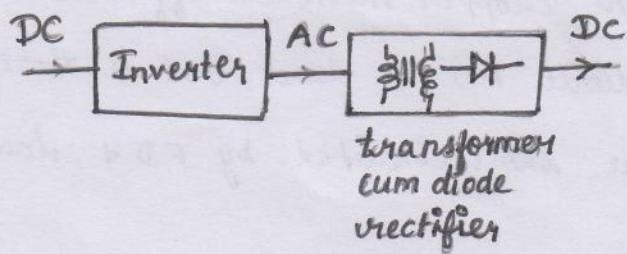


Choppers - Introduction

AC Link chopper.



converted back to dc by a diode rectifier.

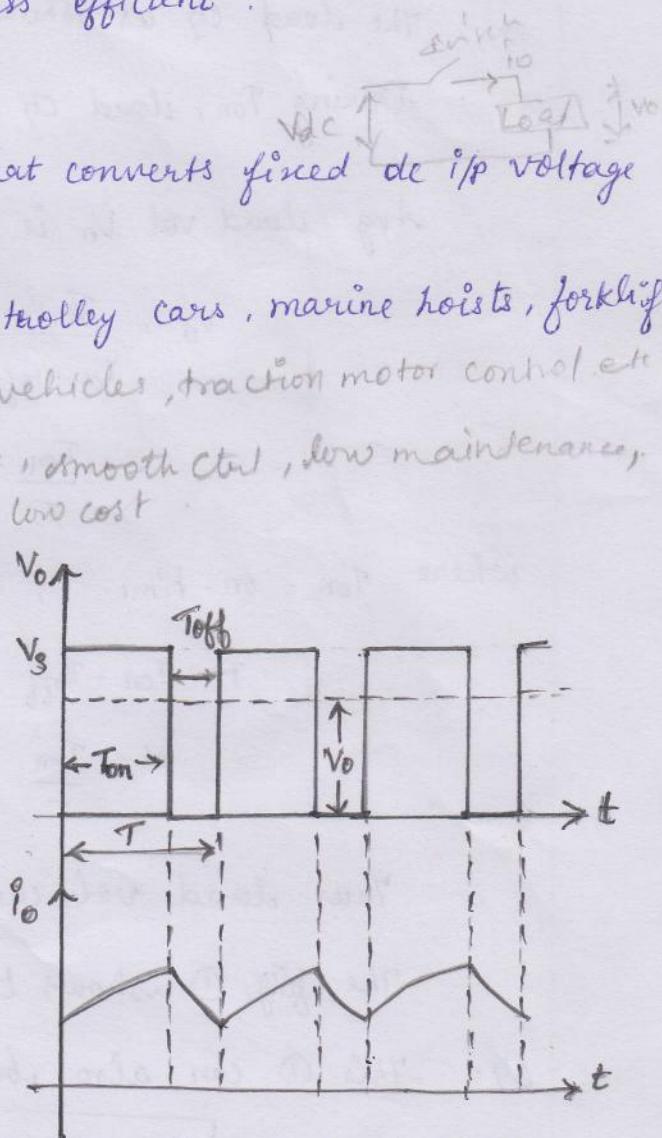
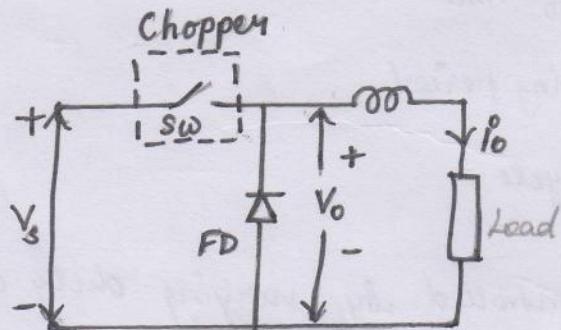
As the conversion is in two stages, dc to ac & then ac to dc, ac link chopper is costly, bulky & less efficient.

DC Chopper

A chopper is a static device that converts fixed dc i/p voltage to a variable dc o/p vol directly.

DC choppers are well used in trolley cars, marine hoists, forklift trucks & mine haulers, battery operated vehicles, traction motor control etc.
Adv: Greater efficiency, faster response, smooth ctrl, low maintenance, low cost.

Step down Chopper.



Chopper is a high speed on/off semiconductor switch.

For the sake of highlighting the principle of chopper operation, the chopper is used for controlling the on, off periods of this switch is not shown.

During the period when chopper is on & load vol is equal to source vol V_s .

During the interval T_{off} , the chopper switches off, load current flows through the freewheeling diode FD. & load vol is therefore zero. As a result, load terminals are short circuited by FD & load voltage is therefore zero during T_{off} .

In this manner, a chopped dc vol is produced at the load terminals.

The load ch as is shown in fig is continuous.

During T_{on} , load ch rises whereas during T_{off} , load ch decays.

Avg load vol V_o is given by

$$V_o = \frac{T_{on}}{T_{on} + T_{off}} V_s$$

$$= \frac{T_{on}}{T} V_s \Rightarrow \boxed{V_o = \delta V_s} \quad \rightarrow \textcircled{1}$$

where T_{on} = on-time ; T_{off} = off-time

$T = T_{on} + T_{off}$ = Chopping period ,

$$\alpha = \frac{T_{on}}{T} = \text{duty cycle} .$$

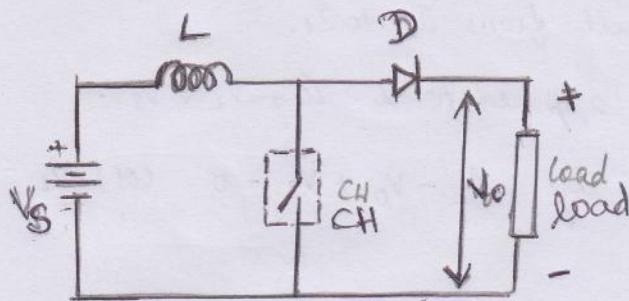
Thus load vol can be controlled by varying duty cycle α .

The fig, ① shows that the load vol is independent of load ch. This ① can also be written as

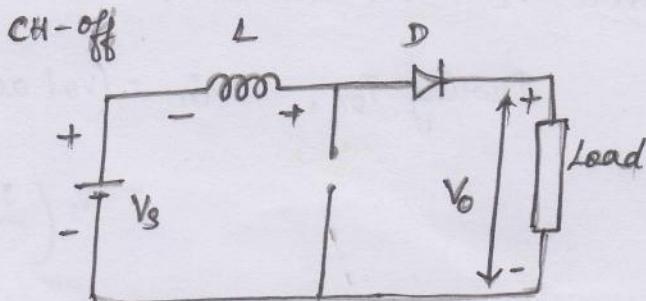
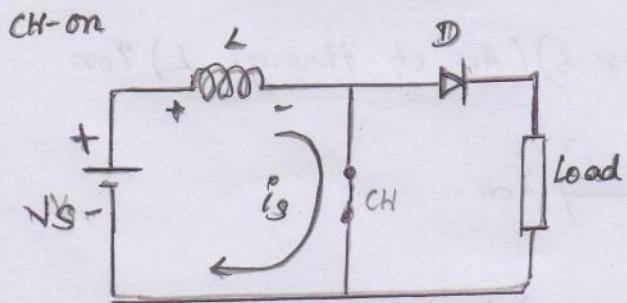
$$\boxed{V_o = f \cdot T_{on} \cdot V_s} \quad \rightarrow \textcircled{2}$$

where ($\because f = \frac{1}{T} = \text{Chopping freq}$)

Step-up Choppers



In step-up chopper, a large inductor L in series with source vol V_s is essential.

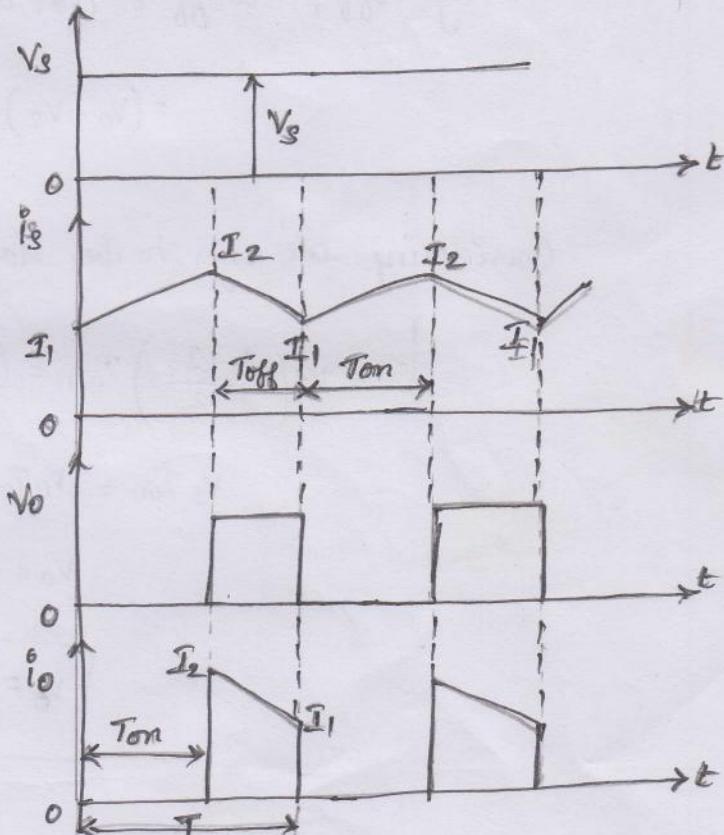


When the chopper CH is on, the closed current path is as shown in fig & the inductor stores energy during T_{on} period.

When CH is off, as the inductor C_L cannot dive down instantaneously, this C_L is forced to flow through the diode & load for a time T_{off} .

As the C_L tends to \downarrow , polarity of the emf induced in L is reversed. As a result, vol across the load, given by $V_o = V_s + L \frac{di}{dt}$, exceeds the source vol V_s .

In this manner, the ckt acts as a step-up chopper & the energy stored in L is released to the load.



When CH is on, ch through L would ↑ from I_1 to I_2 .

When CH is off, ch would fall from I_2 to I_1 .

With CH-on, source vol is applied to L i.e., $V_L = V_S$.

When CH-off, KVL is given by $V_L - V_0 + V_S = 0$ (or) $V_L = V_0 - V_S$.

Here $V_L \rightarrow$ vol across L.

During T_{on} , $W_{in} = \underbrace{(\text{vol across } L)(\text{Avg ch through } L)}_{= V_S \left(\frac{I_1 + I_2}{2} \right)} T_{on}$

$$= V_S \left(\frac{I_1 + I_2}{2} \right) T_{on}.$$

During T_{off} , $W_{off} = (\text{vol across } L)(\text{Avg ch through } L) T_{off}$

$$= (V_0 - V_S) \left(\frac{I_1 + I_2}{2} \right) T_{off}.$$

Considering the sys to be lossless, $W_{in} = W_{off}$.

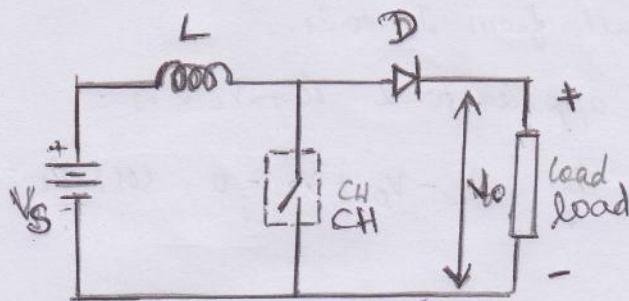
$$\therefore V_S \left(\frac{I_1 + I_2}{2} \right) T_{on} = (V_0 - V_S) \left(\frac{I_1 + I_2}{2} \right) T_{off}$$

$$V_S T_{on} = V_0 T_{off} - V_S T_{off}$$

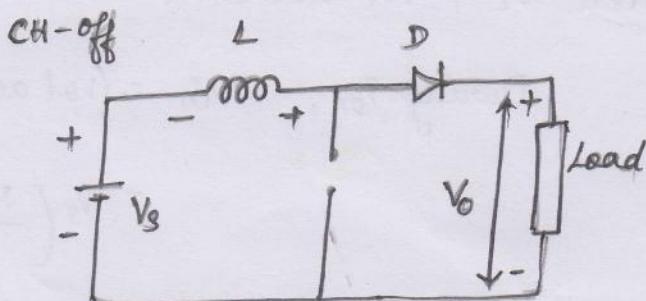
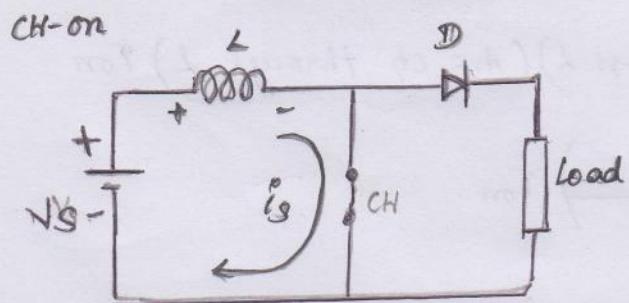
$$V_0 = V_S \frac{T}{T_{off}} = V_S \left(\frac{T}{T - T_{on}} \right)$$

$$V_0 = V_S \left(\frac{1}{1 - \alpha} \right)$$

Step-up Choppers



In step-up chopper, a large inductor L in series with source vol V_s is essential.

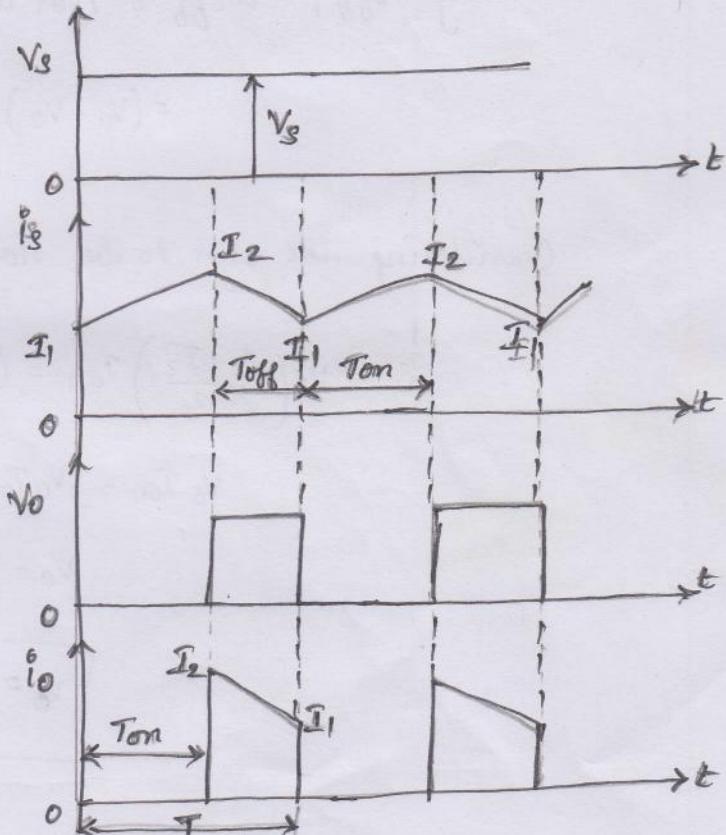


When the chopper CH is on, the closed current path is as shown in fig & the inductor stores energy during T_{on} period.

When CH is off, as the inductor C_L cannot dive down instantaneously, this C_L is forced to flow through the diode & load for a time T_{off} .

As the C_L tends to \downarrow , polarity of the emf induced in L is reversed. As a result, vol across the load, given by $V_o = V_s + L \frac{di}{dt}$, exceeds the source vol V_s .

In this manner, the ckt acts as a step-up chopper & the energy stored in L is released to the load.



Control strategies of Chopper.

$$V_o = V_{dc} \alpha.$$

It is seen that, the avg value of off vol, V_o can be controlled by periodic opening & closing of the switches.

There are 2 types of ctrl strategies. 1) Time-ratio ctrl (TRC) &
2) Ctrl limit ctrl.

Time-Ratio ctrl (TRC)

In the time-ratio ctrl, the value of $\frac{T_{on}}{T}$ is varied.

This is effected in 2 ways.

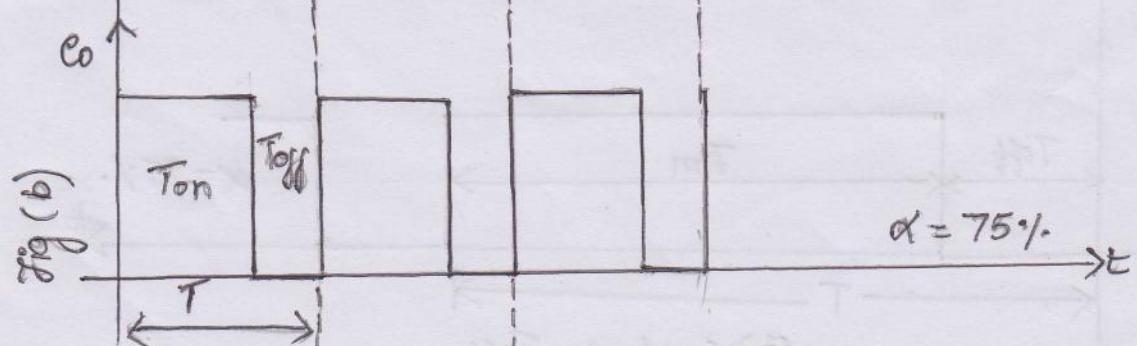
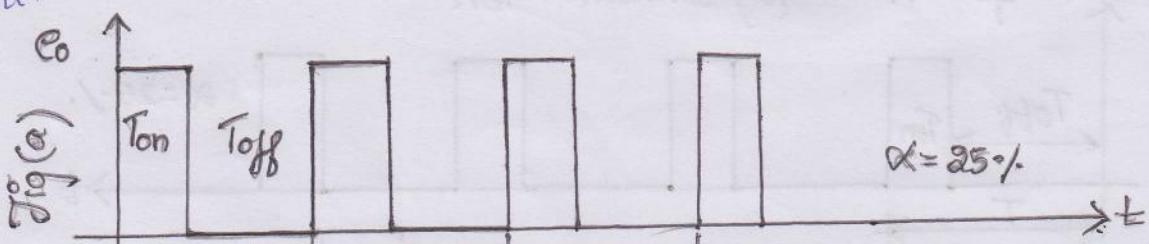
They are variable freq operation & const freq operation.

Const freq ctrl.

In this type of ctrl strategy, the on-time T_{on} , is varied but the chopping freq f ($f = 1/T$, & hence the chopping period T) is kept constant.

Kept constant.

Variation of T_{on} means adjustment of pulse width, as such this control strategy is also called as pulse-width modulation ctrl.



The above fig illustrates the principle of pulse-width modulation.
As shown, chopping period T is constant

$T_{on} = \frac{1}{4} T$, so that duty cycle $\alpha = 25\%$ \Rightarrow from fig (a)

$T_{off} = \frac{3}{4} T$, so that duty cycle $\alpha = 75\%$.

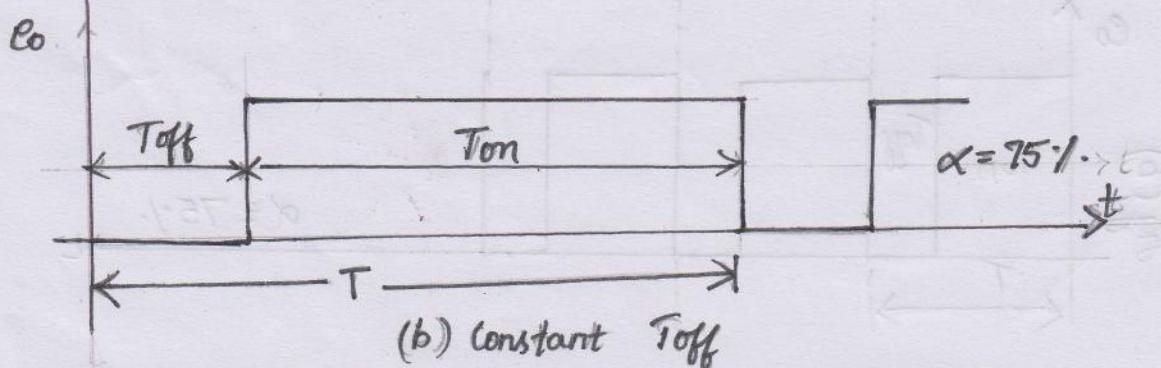
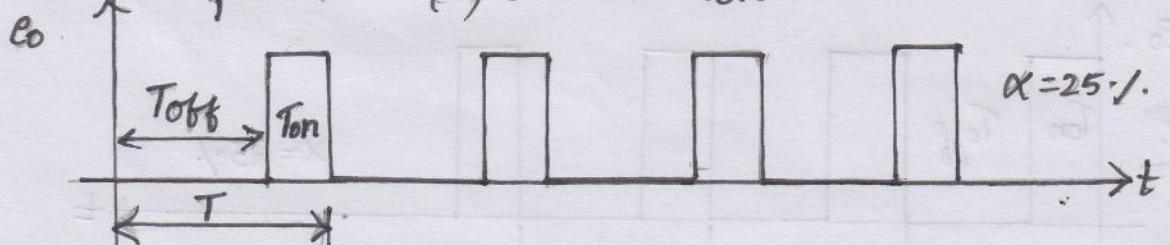
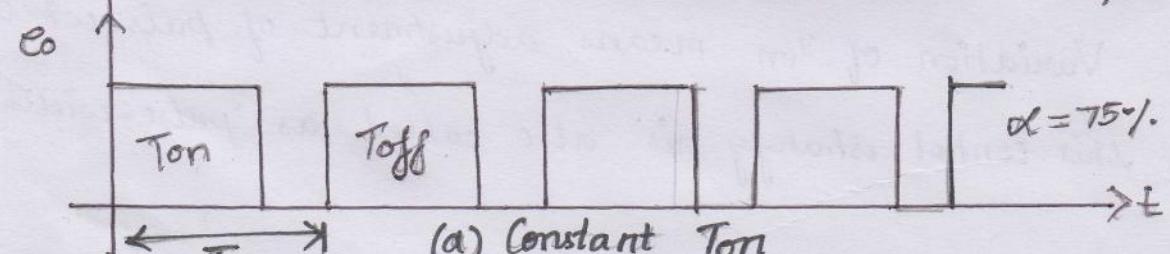
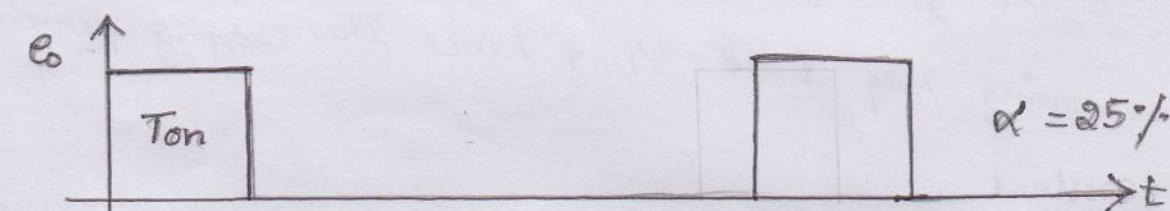
Hence, the o/p vol E_0 can be varied by varying the on-time T_{on} .

Variable freq ctrl.

In this type of ctrl strategy, the chopping frequency f is varied & either

a) On-time T_{on} is kept constant (or) b) off time, T_{off} is kept const.

This type of ctrl strategy is also called as freq modulation ctrl.



As shown in fig (a) chopping period T is varied but on-time T_{on} is kept constant.

The o/p voltage waveforms are shown for two different duty cycles.

In fig (b), chopping period T is varied but T_{off} is kept constant.

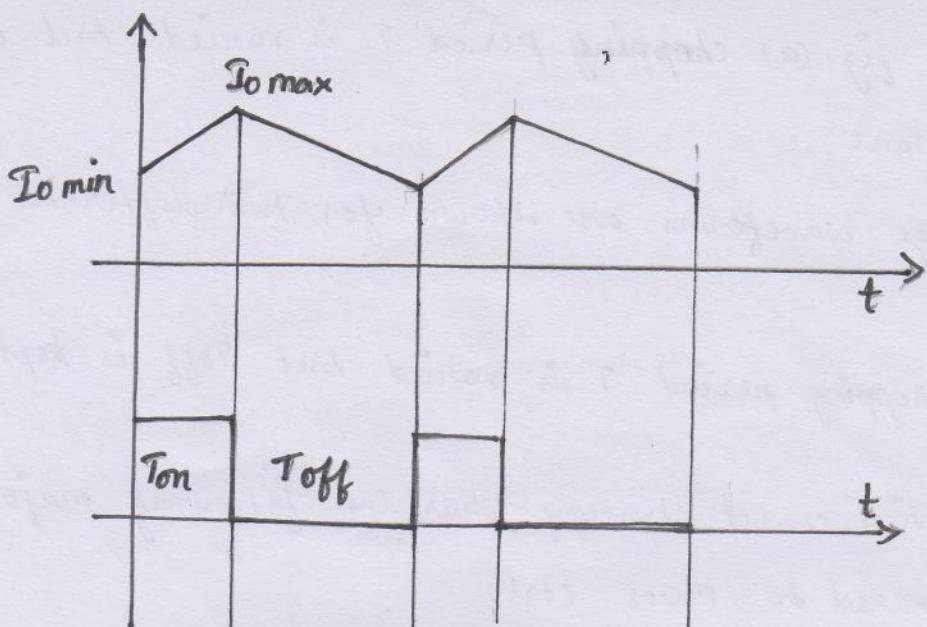
Freq modulation control strategy has the following major disadvantages compared to PWM ctrl.

- (i) The chopping freq has to be varied over a wide range for the ctrl of o/p vol in freq modulation. Filter design for such wide freq variation is, therefore, quite difficult.
- (ii) For the ctrl of duty cycle, freq variation would be wide. As such, there is a possibility of interference with signalling & telephone lines in freq mody technique.
- (iii) The large off-time in freq mody technique may make the dead \leftrightarrow discontinuous, which is undesirable.
Thus, the constant freq sys(PWM) is preferred scheme for chopper drives.

Current limit control.

In current limit control strategy, the chopper is switched ON and OFF so that the current in the load is maintained b/w two limits.

When the current exceeds upper limit, the chopper is switched OFF. During OFF period, the load \leftrightarrow free wheels & decreases exponential.



When it reaches the lower limit, the chopper is switched off.

Current limit control is possible either with const freq or with constant Ton.

The current limit control is used only when the load has energy storage elements.

The reference values are the load current or load-voltage.

The above fig illustrates the principle of current limit ctrl.

Since, the chopper operates b/w prescribed current limits, discontinuity cannot occur.

The difference b/w $I_{0\max}$ & $I_{0\min}$ decides the switching frequency.

The ripple in the load current can be reduced if the difference b/w the $I_{0\max}$ & $I_{0\min}$ limits is min.

This in turn is chopper frequency thereby min the switching losses.

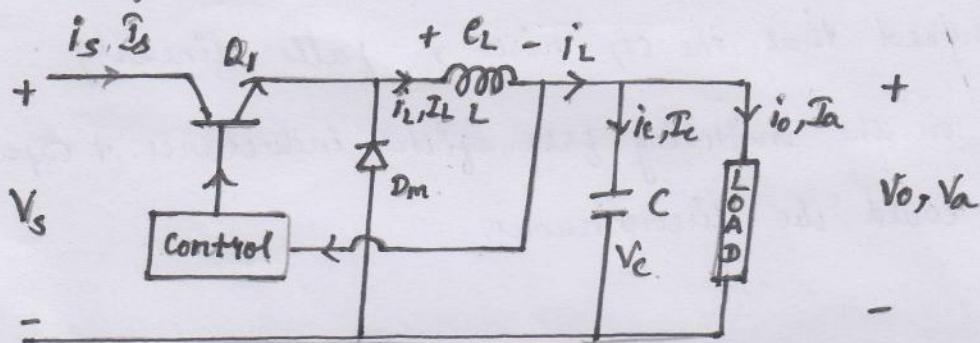
Switching mode regulators

DC converters can be used as switching-mode regulators to convert a dc vol., normally unregulated, to a regulated dc o/p vol.

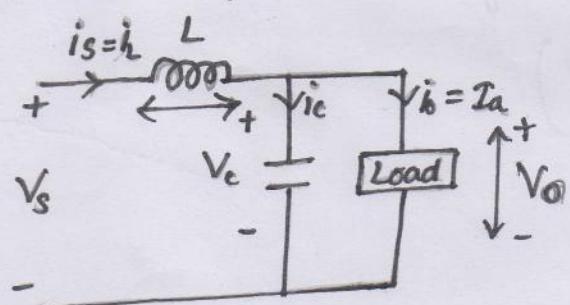
There are 4 basic topologies of switching regulators.

- 1) Buck regulators
- 3) Buck-boost regulators
- 2) Boost regulators
- 4) Cuk-regulators.

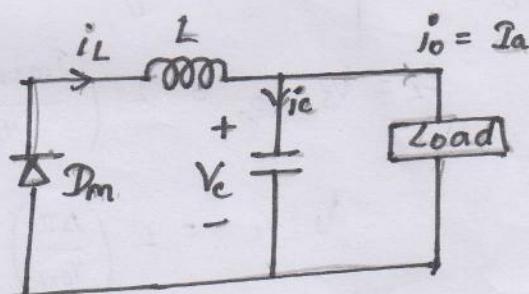
Buck regulators



Mode 1 : Q_1 on



Mode 2 : Q_1 -off



In a buck regulator, the avg o/p vol V_o , is less than the i/p vol, V_s & hence the name "buck".

The ckt operation can be divided into two modes.

Mode 1 begins when Q_1 is switched on at $t=0$.

The i/p sig which rises, flows through filter inductor L, filter cap C, & load resist R.

Mode 2 begins when Q_1 is switched off at $t=t_1$.

The freewheeling diode D_m conducts due to energy stored in the inductor; & the inductor current continues to flow through L , C , load & diode D_m .

The inductor current falls until S_1 is switched on again in the next cycle.

The waveforms for the voltages & currents are shown in fig for a continuous current flow in the inductor L .

It is assumed that the current rises & falls linearly.

Depending on the switching freq., filter inductance, & capacitance the inductor current could be discontinuous.

$$e_L = L \frac{di}{dt}$$

Voltage across L during t_{on} period of the switch

$$e_L = V_S - V_o = L \left(\frac{I_{max} - I_{min}}{T_{on}} \right)$$

$$V_S - V_o = L \left(\frac{\Delta I}{T_{on}} \right)$$

$$T_{on} = L \left(\frac{\Delta I}{V_S - V_o} \right) \rightarrow ①$$

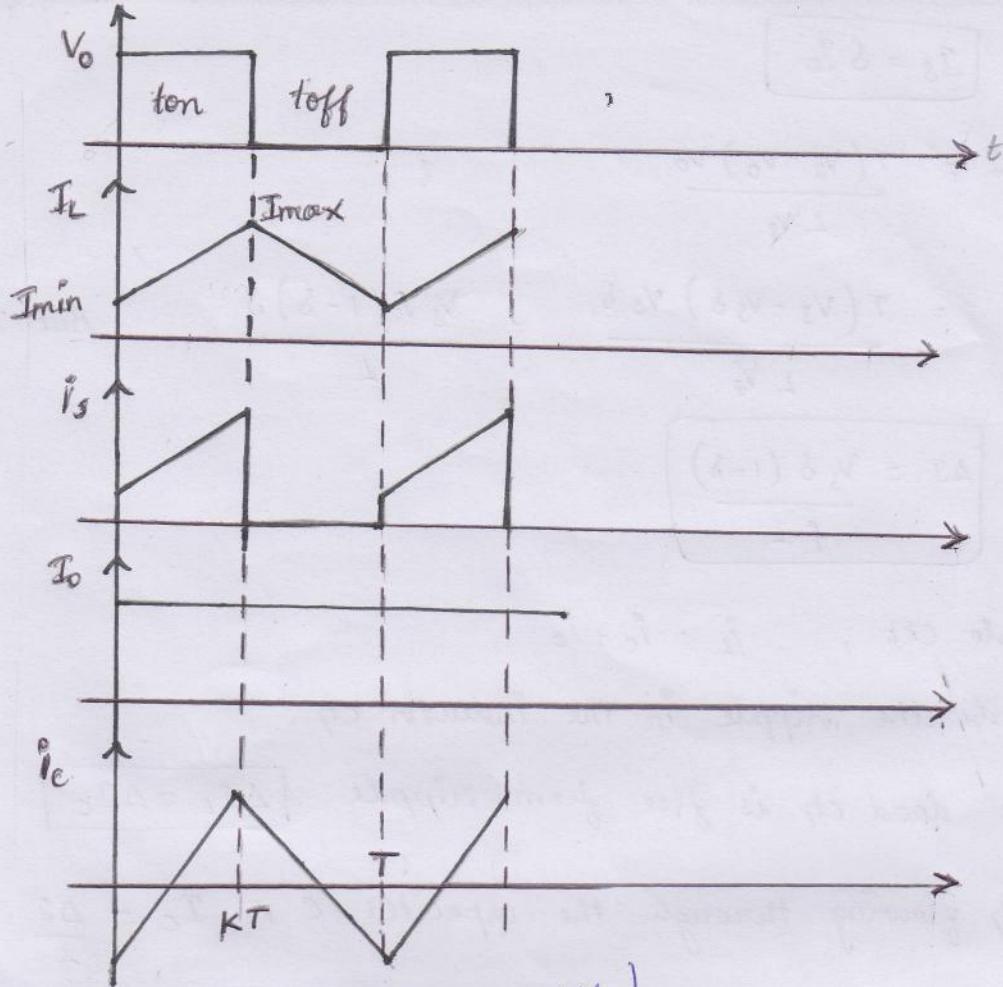
Voltage across L during T_{off} .

$$e_L = V_o = L \frac{\Delta I}{T_{off}}$$

$$T_{off} = L \left(\frac{\Delta I}{V_o} \right) \rightarrow ②$$

$$\text{From } ① \Rightarrow \Delta I = \frac{T_{on} (V_S - V_o)}{L} \rightarrow ③$$

$$\text{From } ② \Rightarrow \Delta I = T_{off} \left(\frac{V_o}{L} \right) \rightarrow ④$$



$$\textcircled{3} = \textcircled{4} \quad \frac{ton(V_s - V_o)}{4} = toff \left(\frac{V_o}{K} \right)$$

$$ton V_s - ton V_o = toff V_o$$

$$ton V_s = V_o (ton + toff)$$

$$V_o = \left(\frac{ton}{ton + toff} \right) V_s \Rightarrow \boxed{V_o = 8 V_s}$$

Total time period $T = ton + toff$

addl. \textcircled{1} + \textcircled{2}

$$T = L \left(\frac{\Delta I}{V_s - V_o} \right) + L \left(\frac{\Delta I}{V_o} \right)$$

$$T = L \Delta I \left(\frac{1}{V_s - V_o} + \frac{1}{V_o} \right)$$

$$\boxed{T = L \Delta I \left[\frac{V_s}{(V_s - V_o)V_o} \right]} \longrightarrow \textcircled{3}$$

Assuming the sys to be loss less,

$$V_s I_s = V_o I_o$$

$$V_s I_s = V_s \delta I_o \quad (\because V_o = V_s \delta)$$

$$I_s = \delta I_0$$

From ⑤

$$\Delta I = \frac{T(V_s - V_o) V_o}{L V_s} = \frac{T(V_s - V_s \delta) V_s \delta}{L V_s} = \frac{V_s T (1-\delta) \delta}{L}$$

But $T = \frac{1}{f}$

$$\therefore \Delta I = \frac{V_s \delta (1-\delta)}{f L}$$

Apply KCL to ckt, $i_L = i_c + i_o$

Let δI_L be the ripple in the inductor i_L .

Assuming the load ckt is free from ripple $[\Delta I_L = \Delta I_c]$

The Avg ckt flowing through the capacitor C , $I_C = \frac{\Delta I}{4}$

The vol across the capacitor C

$$V_C = \frac{1}{C} \int_0^{T/2} I_C dt + V_C(t=0)$$

$$\Delta V_C = V_C - V_C(t=0) = \frac{1}{C} \int_0^{T/2} I_C dt.$$

$$\Delta V_C = \frac{1}{C} \frac{\Delta I}{4} \int_0^{T/2} dt.$$

$$= \frac{1}{C} \frac{\Delta I}{4} [t]_0^{T/2}$$

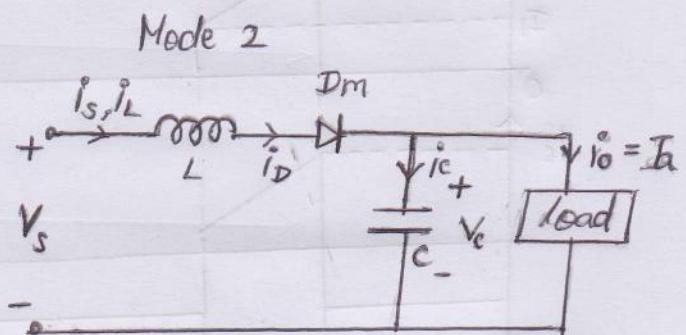
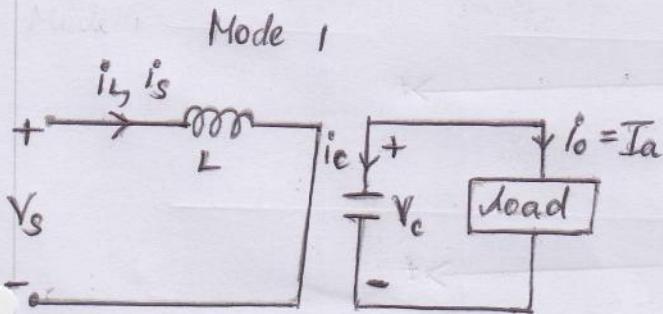
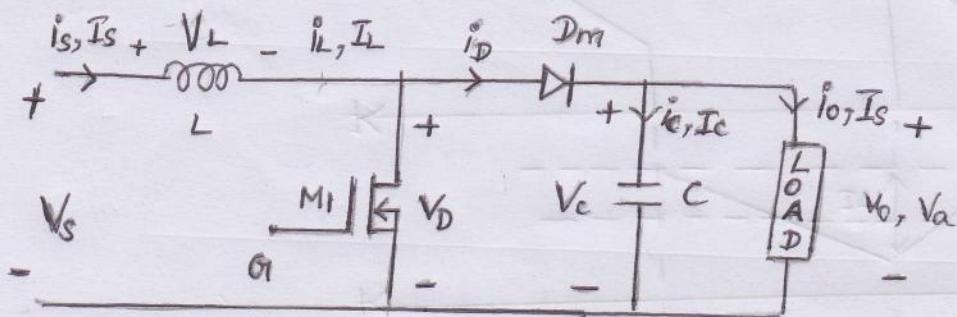
$$\Delta V_C = \frac{1}{C} \frac{\Delta I}{4} \left[\frac{T}{2} \right]$$

$$= \frac{\Delta I}{\delta C} \left(\frac{1}{f} \right)$$

$$\text{Sub } \Delta I \quad \therefore \Delta V_C = \frac{1}{\delta C f} \left[\frac{V_s \delta (1-\delta)}{f L} \right]$$

$$\boxed{\Delta V_C = \frac{V_s \delta (1-\delta)}{8 C f^2 L}}$$

Boost regulators.



In boost regulator, the o/p vol is greater than the i/p vol hence the name "boost".

The ckt operation can be divided into two modes.

Mode 1 begins when M_1 is switched on at $t=0$.

The i/p ct, which rises, flows through $L + Q_1$.

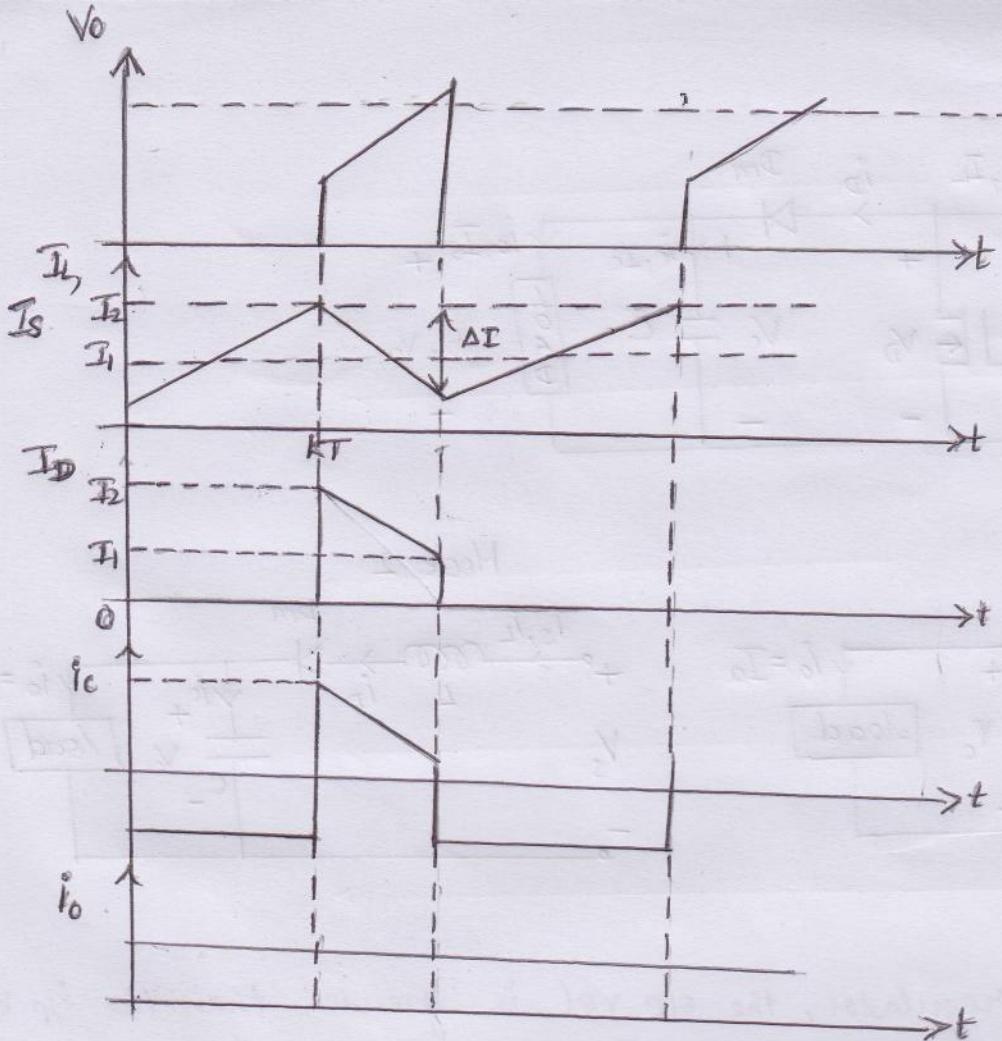
Mode 2 begins when M_1 is switched off at $t=t_1$.

The ct that was flowing through the transistor would now flow through L, C, load and diode D_m .

The L ct falls until M_1 is turned on again in the next cycle.

The energy stored in L is transferred to the load.

The waveforms for vol & cts are shown in fig for continuous load ct assuming that the ct rises or falls linearly.



Voltage across L $V_L = L \frac{di}{dt}$

Vol across L during T_{on} of $T_1 \Rightarrow V_S = L \left(\frac{I_{max} - I_{min}}{T_{on}} \right)$

$$= L \frac{\Delta I}{T_{on}}$$

$$T_{on} = \frac{L \Delta I}{V_S} \quad \rightarrow \textcircled{1}$$

Vol across L during T_{off} of T_1

$$V_S - V_0 = V_L = -L \frac{\Delta I}{T_{off}}$$

$$T_{off} = -\frac{L \Delta I}{V_S - V_0} \quad \rightarrow \textcircled{2}$$

From \textcircled{1} $\Delta I = \frac{T_{on} V_S}{L} \quad \rightarrow \textcircled{3}$

From \textcircled{2} $\Delta I = -T_{off} \frac{(V_S - V_0)}{L} \quad \rightarrow \textcircled{4}$

$$\textcircled{1} = \textcircled{2} \quad T_{on} \frac{V_s}{L} = -\frac{T_{off} (V_s - V_o)}{L}$$

$$T_{on} V_s = -T_{off} V_s + T_{off} V_o$$

$$V_s (T_{on} + T_{off}) = V_o T_{off}$$

$$V_o = V_s \left(\frac{T_{on} + T_{off}}{T_{off}} \right) = V_s \left(\frac{T}{T_{off}} \right)$$

$$= V_s \left(\frac{T}{T - T_{on}} \right)$$

$$= V_s \left(\frac{1}{\frac{T}{T} - \frac{T_{on}}{T}} \right)$$

$$V_o = V_s \left(\frac{1}{1 - \delta} \right)$$

From \textcircled{1} & \textcircled{2}.

$$\text{Total time period } T = T_{on} + T_{off}$$

$$T = L \left(\frac{\Delta I}{V_s} \right) + -L \left(\frac{\Delta I}{V_s - V_o} \right)$$

$$= L \Delta I \left(\frac{1}{V_s} - \frac{1}{V_s - V_o} \right)$$

$$= L \Delta I \left[\frac{V_s - V_o - V_s}{V_s(V_s - V_o)} \right] = L \Delta I \left(\frac{-V_o}{V_s(V_s - V_o)} \right)$$

$$T = \frac{1}{f} = \frac{L \Delta I V_o}{V_o (V_o - V_s)}$$

$$\text{Sub } V_o = \frac{V_s}{1 - \delta} \quad \therefore T = \frac{L \Delta I \left(\frac{V_s}{1 - \delta} \right)}{\frac{V_s}{1 - \delta} \left[\left(\frac{V_s}{1 - \delta} \right) - V_s \right]} = \frac{L \Delta I \left[\frac{V_s}{1 - \delta} \right]}{\frac{V_s^2}{1 - \delta} \left[\frac{1}{1 - \delta} - 1 \right]}$$

$$= \frac{L \Delta I}{(1 - \delta) V_s \left[\frac{1 - 1 + \delta}{1 - \delta} \right]} = \frac{L \Delta I}{(1 - \delta) V_s \left(\frac{\delta}{1 - \delta} \right)}$$

$$T = \frac{1}{f} = \frac{L \Delta I}{V_s \delta}$$

$$\boxed{\Delta I = \frac{V_s \delta}{L f}}$$

Assuming the system to be loss less $V_s I_s = V_0 I_0$

$$V_s I_s = \left(\frac{V_s}{1-\delta} \right) I_0$$

$$\boxed{I_s = \frac{I_0}{1-\delta}}$$

Capacitor vol

$$V_c = \frac{1}{C} \int_0^{t_{on}} i_c dt + V_c(t=0)$$

\therefore the capacitor is initially in charged condn.

$$V_c - V_c(t=0) = \frac{1}{C} \int_0^{t_{on}} i_c dt$$

$$\begin{aligned} \Delta V_c &= \frac{1}{C} \int_0^{t_{on}} i_c dt \\ &= \frac{1}{C} \int_0^{t_{on}} I_0 dt = \frac{I_0}{C} [t]_0^{t_{on}}. \end{aligned}$$

During t_{on} , capacitor is supplying C_0 to load. $\therefore \dot{i}_0 = \dot{i}_c$

$$\Delta V_c = \frac{I_0}{C} [t]_0^{t_{on}} = \frac{I_0}{C} [t_{on}]$$

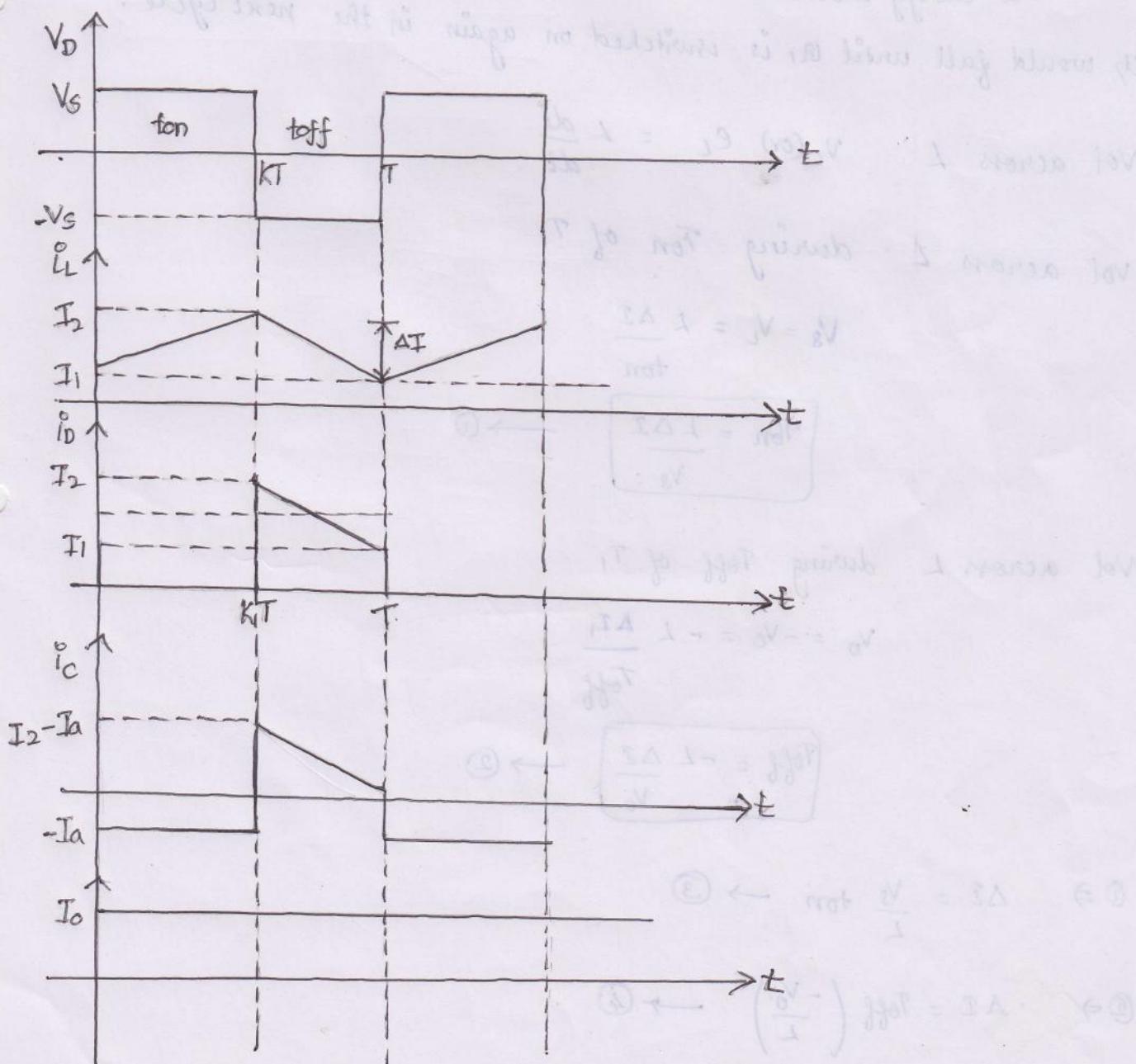
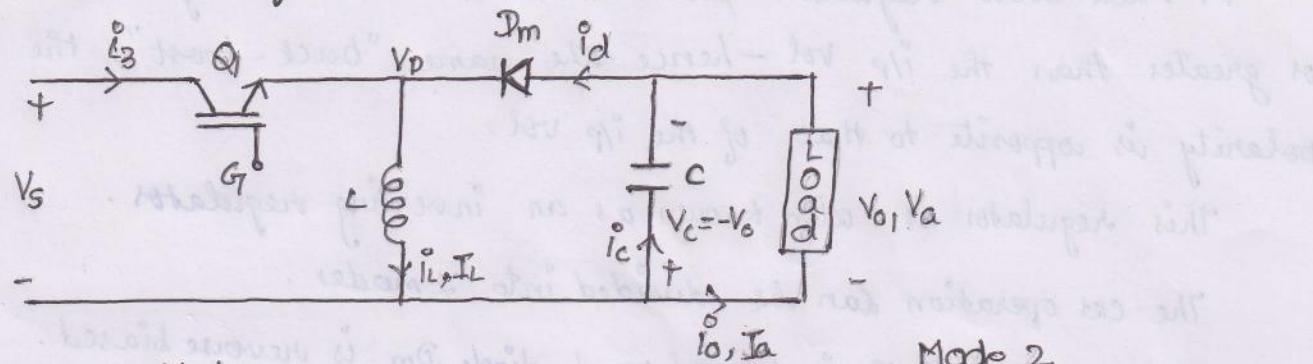
$$\text{Substitute } t_{on} \Rightarrow \Delta V_c = \frac{I_0}{C} \left[\frac{L \Delta I}{V_s} \right]$$

Sub $\Delta I \Rightarrow$

$$\Delta V_c = \frac{I_0}{C} \frac{L}{V_s} \left[\frac{V_s \delta}{L f} \right]$$

$$\boxed{\Delta V_c = \frac{I_0 \delta}{C f}}$$

Buck-boost Regulators.



A buck-boost regulator provides an o/p vol that may be less than or greater than the i/p vol - hence the name "buck-boost"; the o/p vol polarity is opposite to that of the i/p vol.

This regulator is also known as an inverting regulator.

The ckt operation can be divided into 2 modes.

During mode 1, Q_1 is turned on & diode D_m is reverse biased.

The i/p ct, which rises, flows through L & Q_1 .

During mode 2, Q_1 is switched off & the ct, which was flowing through L , would flow through L , C , D_m & the load.

The energy stored in L would be transferred to the load & the inductor ct would fall until Q_1 is switched on again in the next cycle.

$$\text{Vol across } L \quad V_L(\text{or}) \quad e_L = L \frac{di}{dt}$$

Vol across L during Ton of T_1

$$V_S = V_L = L \frac{\Delta I}{T_{on}}$$

$$T_{on} = \frac{L \Delta I}{V_S} \rightarrow ①$$

Vol across L during Toff of T_1

$$V_0 = -V_C = -L \frac{\Delta I_1}{T_{off}}$$

$$T_{off} = -L \frac{\Delta I}{V_0} \rightarrow ②$$

$$③ \Rightarrow \Delta I = \frac{V_S}{L} T_{on} \rightarrow ③$$

$$④ \Rightarrow \Delta I = T_{off} \left(-\frac{V_0}{L} \right) \rightarrow ④$$

$$\textcircled{3} = \textcircled{4}$$

$$\frac{V_s}{L} t_{on} = T_{off} \left(\frac{-V_0}{L} \right)$$

$$V_0 = -V_s \left(\frac{T_{on}}{T_{off}} \right)$$

$$= -V_s \left(\frac{t_{on}}{T - t_{on}} \right)$$

$$= -V_s \left(\frac{\frac{1}{\frac{T}{t_{on}} - 1}} \right) = -V_s \left(\frac{\frac{1}{\frac{1}{\delta} - 1}} \right)$$

$$V_0 = -V_s \left(\frac{\delta}{1-\delta} \right)$$

$$\text{Total time period } T = T_{on} + T_{off}.$$

$$= L \frac{\Delta I}{V_s} + -L \frac{\Delta I}{V_0}$$

$$= L \Delta I \left(\frac{1}{V_s} - \frac{1}{V_0} \right)$$

$$\frac{1}{f} = T = L \Delta I \left[\frac{V_0 - V_s}{V_0 V_s} \right]$$

$$\text{put } V_0 = -V_s \frac{\delta}{1-\delta}$$

$$\therefore T = \frac{1}{f} = L \Delta I \left[\frac{\frac{-V_s \delta}{1-\delta} - V_s}{\left(\frac{-V_s \delta}{1-\delta} \right) V_s} \right]$$

$$= L \Delta I \left[\frac{-V_s \left[\left(\frac{\delta}{1-\delta} \right) + 1 \right]}{\frac{-V_s^2 \delta}{1-\delta}} \right]$$

$$= L \Delta I \left[\frac{1 + \frac{\delta}{1-\delta}}{\frac{V_B \delta}{1-\delta}} \right],$$

$$= L \Delta I \left[\frac{\frac{1-\delta + \delta}{1-\delta}}{\frac{V_B \delta}{1-\delta}} \right]$$

$$\boxed{T = L \Delta I \left[\frac{1}{V_B \delta} \right]}$$

Sub $T = \frac{1}{f} \Rightarrow \frac{1}{f} = L \Delta I \left(\frac{1}{V_B \delta} \right)$

$$\boxed{\Delta I = \frac{V_B \delta}{L f}}$$

Assuming the system to be loss less

$$V_B I_S = - V_0 I_0$$

$$V_B I_S = + \left[\frac{L V_B \delta}{1-\delta} \right] I_0.$$

$$\boxed{I_S = \left(\frac{\delta}{1-\delta} \right) I_0}$$

Capacitor voltage \downarrow_{ton}

$$V_C = \frac{1}{C} \int_0^{ton} i_C dt + V_C(t=0)$$

$$V_C - V_C(t=0) = \frac{1}{C} \int_0^{ton} i_C dt$$

$$\Delta V_C = \frac{I_C}{C} [ton]$$

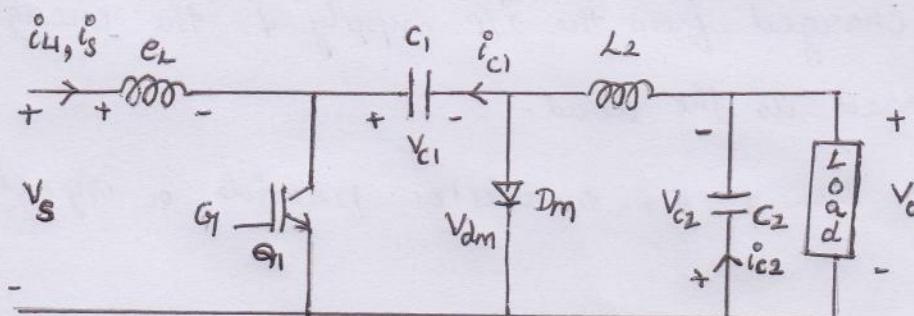
Substitute for ΔI $\Delta V_C = \frac{I_C}{C} \left[\frac{L \Delta I}{V_S} \right]$

Sub ΔI $\Delta V_C = \frac{I_C}{C} \left[\frac{L}{V_S} \left(\frac{V_S \delta}{L_f} \right) \right]$

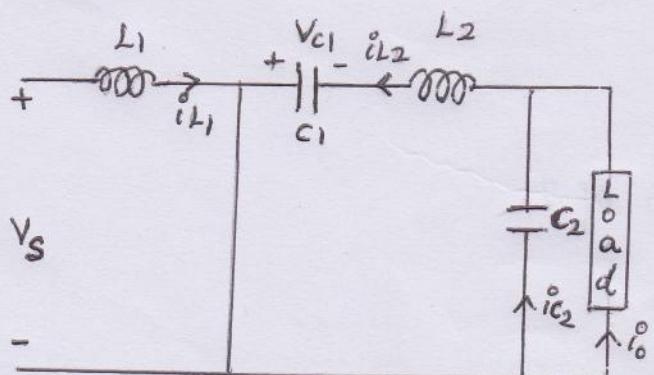
$$\Delta V_C = \frac{I_C}{C} \left(\frac{\delta}{f} \right) \quad \text{Put } I_C = -I_0$$

$$\therefore \boxed{\Delta V_C = -\frac{I_0 \delta}{C f}}$$

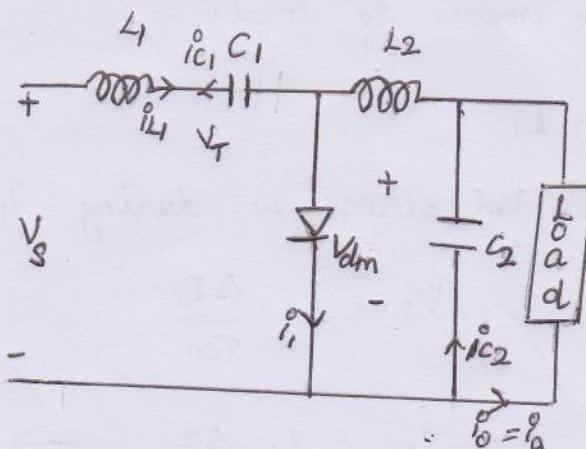
Cuk Regulator.



Mode 1 :



Mode 2 :



The ckt arrangement of the cuk regulator using a power BJT is shown in fig.

Like to the buck-boost regulator, the cuk regulator provides an o/p vol that is less than or greater than the i/p vol, but the o/p vol polarity is opposite to that of the i/p vol.

When the i/p vol is turned on & Q_1 is switched off, diode D_m is fwd biased & C_1 is charged through L_1 , D_m , & the i/p supply V_s .

The ckt operation can be divided into 2 modes.

Mode 1 begins when Q_1 is turned on at $t = 0$.

The C_1 through L_1 rises.

At the same time, the vol of C_1 reverse biases diode D_m turns it off.

The capacitor C_1 discharges its energy to the ckt formed by C_1 , C_2 , the load & L_2 .

Mode 2 begins when Q_1 is turned off at $t = t_1$.

The C_1 is charged from the i/p supply & the energy stored in L_2 is transferred to the load.

The diode D_m & Q_1 transistor provide a synchronous switching action.

The capacitor C_1 is the medium for transferring energy from the source to load.

For L_1

Vol across L_1 during T_{on} of T_1

$$V_s = L_1 \frac{\Delta I_1}{T_{on}}$$

$$T_{on} = L_1 \frac{\Delta I_1}{V_s} \rightarrow ①$$

During T_{off} of T_1 through L falls from I_{max} to I_{min} , & voltage across L during T_{off} is $V_s - V_C$,

$$V_s - V_{C1} = -L_1 \frac{\Delta I_1}{T_{off}}$$

$$T_{off} = -\left(\frac{L_1 \Delta I_1}{V_s - V_{C1}}\right) \rightarrow ②.$$

$$① \Rightarrow \Delta I_1 = \frac{T_{on} V_s}{L_1} \rightarrow ③$$

$$② \Rightarrow \Delta I_1 = \frac{T_{off} (V_{C1} - V_s)}{L_1} \rightarrow ④$$

③ = ④ \Rightarrow Avg voltage across capacitor C_1

$$\Delta I_1 = V_s t_{on} = (V_{C1} - V_s) t_{off}$$

$$V_s t_{on} = V_{C1} t_{off} - V_s t_{off}$$

$$V_s (t_{on} + t_{off}) = V_{C1} t_{off}$$

$$V_{C1} = V_s \left(\frac{T_{on} + T_{off}}{T_{off}} \right)$$

$$= V_s \left(\frac{T}{T - T_{on}} \right)$$

$$= V_s \left(\frac{\frac{T}{T - T_{on}}}{1 - \frac{T_{on}}{T}} \right)$$

$$= V_s \left(\frac{1}{1 - \frac{T_{on}}{T}} \right)$$

$$V_{C1} = V_s \left(\frac{1}{1 - \delta} \right) \rightarrow ⑤.$$

During Ton of T_1 , current through L_2 rises from I_{min2} to I_{max2}

$$i_{L2} \Delta I_2 = I_{max2} - I_{min2}$$

Voltage across L_2 during on period of T_1

$$V_{C1} - V_{C2} = L_2 \frac{\Delta I_2}{T_{on}} \quad \boxed{-V_{C2} = V_o}$$

$$T_{on} = \frac{\Delta I_2 L_2}{V_{C1} + V_o} \rightarrow \textcircled{4}$$

During Toff current through the L falls from I_{max2} to I_{min2}

$$V_o = -L_2 \frac{\Delta I_2}{T_{off}}$$

$$T_{off} = -\frac{L_2 \Delta I_2}{V_o} \rightarrow \textcircled{5}$$

$$\textcircled{4} \Rightarrow \Delta I_2 = \frac{T_{on} (V_{C1} + V_o)}{L_2}$$

$$\textcircled{5} \Rightarrow \Delta I_2 = -\frac{T_{off} V_o}{L_2}$$

$$\textcircled{4} = \textcircled{5} \quad \frac{T_{on} (V_{C1} + V_o)}{L_2} = -\frac{T_{off} V_o}{L_2}$$

$$T_{on} V_{C1} + T_{on} V_o = -T_{off} V_o$$

$$V_o (T_{on} + T_{off}) = -T_{on} V_{C1}$$

$$V_{C1} = \frac{-V_o (T_{on} + T_{off})}{T_{on}}$$

$$V_{CI} = -V_0 \left(\frac{T}{T_{on}} \right),$$

$$= -V_0 \left(\frac{1}{\frac{T_{on}}{T}} \right)$$

$$= -V_0 / \delta$$

$$\therefore V_{CI} = -\frac{V_0}{\delta} \quad \rightarrow \textcircled{6}$$

From ③ & ⑥

$$V_S \left(\frac{1}{1-\delta} \right) = -\frac{V_0}{\delta}$$

$$V_0 = \frac{V_S \delta}{1-\delta} \quad \rightarrow \textcircled{7}$$

Considering the sys to be loss less

$$V_S I_S = -V_0 Z_0.$$

$$\text{sub } V_0 \quad V_S I_S = -\left(\frac{-V_S \delta}{1-\delta} \right) Z_0$$

$$I_S = \frac{Z_0 \delta}{1-\delta} \quad \rightarrow \textcircled{8}$$

Total time period for L_1 $T = t_{on} + t_{off}$

$$= \frac{L_1 \Delta I_1}{V_S} + \frac{-L_1 \Delta I_1}{V_S - V_{CI}}$$

$$= L_1 \Delta I_1 \left(\frac{1}{V_S} - \frac{1}{V_S - V_{CI}} \right)$$

$$= L_1 \Delta I_1 \left(\frac{V_S - V_{CI} - V_S}{V_S(V_S - V_{CI})} \right)$$

$$\frac{1}{f} = \tau = L_1 \Delta I_1 \left(\frac{\frac{V_{C1}}{V_S(V_S - V_{C1})}}{} \right)$$

$$\Rightarrow \Delta I_1 = \underbrace{-V_S(V_S - V_{C1})}_{f V_{C1} L_1}$$

$$\Delta I_1 = \frac{V_S(V_{C1} - V_S)}{f V_{C1} L_1}$$

but $V_{C1} = \frac{V_S}{1-\delta}$ \therefore sub $V_{C1} \Rightarrow \Delta I_1 = \underbrace{V_S \left(\frac{V_S}{1-\delta} - V_S \right)}_{f \left(\frac{V_S}{1-\delta} \right) L_1}$

$$\Delta I_1 = \frac{V_S \delta}{f L_1}$$

peak to peak ripple of in L_1 $\Delta I_1 = \frac{V_S}{f L_1} \rightarrow \textcircled{10}$.

Total time for L_2 $\tau = T_{on} + T_{off}$

$$= \frac{\Delta I_2 L_2}{V_{C1} + V_0} - \frac{L_2 \Delta I_2}{V_0}$$

$$= L_2 \Delta I_2 \left[\frac{1}{V_{C1} + V_0} - \frac{1}{V_0} \right]$$

$$= L_2 \Delta I_2 \left[\frac{V_0 - V_{C1} - V_0}{V_0 (V_{C1} + V_0)} \right]$$

$$\boxed{\frac{1}{f} = \tau = \frac{-L_2 \Delta I_2 V_{C1}}{V_0 (V_{C1} + V_0)}} \rightarrow \textcircled{11}.$$

peak to peak ripple ch in L_2

$$\textcircled{10} \Rightarrow \Delta I_2 = -\frac{V_o(V_{C1} + V_o)}{L_2 V_{e,f}}$$

Sub \textcircled{3} & \textcircled{7}

$$\Delta I_2 = -\frac{\left[\frac{V_S}{1-\delta} + \left(\frac{-V_S \delta}{1-\delta} \right) \right] \left(\frac{-V_S \delta}{1-\delta} \right)}{L_2 \left[\frac{V_S}{1-\delta} \right] f}$$

$$= \frac{-1}{L_2 f} \left[\left(\frac{V_S}{1-\delta} - \frac{V_S \delta}{1-\delta} \right) \left(\frac{-V_S \delta}{1-\delta} \right) \right]$$

$$= \frac{-1}{L_2 f} \left[\left(\frac{-V_S \delta}{1-\delta} \right) \frac{V_S (1-\delta)}{\frac{V_S}{1-\delta}} \right]$$

$$= \frac{-1}{L_2 f} [-V_S \delta]$$

$$\boxed{\Delta I_2 = \frac{V_S \delta}{L_2 f}} \rightarrow \textcircled{12}$$

When T_1 is off, C_1 is charged by i/p ch for $T = T_{off}$.

Avg charging ch $I_C = I_S$

peak to peak ripple voltage of C_1

$$\Delta V_{C1} = \frac{1}{C_1} \int_0^{T_{off}} I_C dt = \frac{1}{C_1} \int_0^{T_{off}} I_S dt = \frac{I}{C_1} [T_{off}]$$

Sub ②

$$\therefore \Delta V_{C1} = \frac{I_s}{C_1} \left[\frac{2L_1 \Delta I_1}{V_s - V_{C1}} \right]$$

sub the value of V_{C1} ③.

$$\Delta V_{C1} = \frac{-I_s L_1 \Delta I_1}{C_1 \left[V_s - \frac{V_s}{1-\delta} \right]} = \frac{-I_s L_1 \Delta I_1}{C_1 V_s \left[\frac{1-\delta-1}{1-\delta} \right]} = \frac{-I_s L_1 \Delta I_1}{-C_1 V_s \left(\frac{\delta}{1-\delta} \right)}$$

sub ΔI_1 ⑩

$$\Delta V_{C1} = \frac{I_s L_1 (1-\delta)}{C_1 V_s \delta} \left[\frac{V_s \delta}{L_1 f} \right]$$

$$\boxed{\Delta V_{C1} = \frac{I_s (1-\delta)}{C_1 f}} \rightarrow ⑬$$

If we assume that load ripple C_L so I_o is negligible.

$$\therefore \Delta I_{L2} = \Delta I_{C2} + \text{?}^b \Rightarrow \boxed{\Delta I_{L2} = \Delta I_{C2}}$$

Average charging C_L of capacitor C_2 which flows for time $T/2$ becomes $I_{C2} = \frac{\Delta I_2}{4}$

Peak to peak ripple voltage across C_2

$$\begin{aligned} \Delta V_{C2} &= \frac{1}{C_2} \int_0^{T/2} I_{C2} dt = \frac{1}{C_2} \int_0^{T/2} \frac{\Delta I_2}{4} dt = \frac{\Delta I_2}{4 C_2} \left[\frac{T}{2} \right] \\ &= \frac{\Delta I_2}{8 C_2} \left[\frac{1}{f} \right] \end{aligned}$$

$$\text{But } \Delta I_2 = \frac{V_s \delta}{L_2 f} \quad \therefore \Delta V_{C2} = \frac{1}{8 f C_2} \left(\frac{V_s \delta}{L_2 f} \right)$$

$$\boxed{\Delta V_{C2} = \frac{V_s \delta}{8 f^2 L_2 C_2}} \rightarrow ⑭$$